MSTronic Co., Ltd.

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MIT-88G-56D-BT

5G PoE Injector

1. INPUT :

1.1 Input Voltage: AC 100V ~ 240V

1.2 Input Frequency : 50 /60hz

1.3 Input Current: 1.21A at 100Vac @F.L
0.78A at 240Vac

1.4 Inrush current: 25A max at 120Vac & 60A max at 230Vac

2. OUTPUT :

2.1 Output Voltage & Current:

OUTPUT	56V
Max. load	1.34A
Power	75W
Min. Load	0A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	2%

TOTAL POWER : 75 W

Note 1: Ripple & Noise bandwidth is from DC to 20Mhz. Terminated With a 47uF Capacitor and 0.1uf MPE Capacitor of Proper Polarity.

3. EFFICIENCY : 88% min. at AC 120V Input @F.L

4. PROTECTION

4.1 Short Circuit Protection
Output Short GND Terminal will not damage the Power Supply will Auto-Recover when Load status going to normally.

4.2 Over Voltage Protection : 59V ~ 65V

4.3 Over Current Limits : PSE Maximum Delivered Power, Per-Port(Table1 & 2)

4.4 LED indicates : JP2

4.5 Hold up time: 6mS min. at maximum load &120Vac Input.

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5.1 EMC: EN55032 Class A, FCC Class A

5.2 SAFETY: IEC60950-1, EN 60950-1 (2nd Edition)

5.3 Efficiency: no meet Level 6 (USA-VI)

6. GENERAL DESCRIPTION

6.1	Operation Temperature:	-25 - +50 °C
6.2	Storage Temperature:	-40 - +85 °C
6.3	Operation Humidity:	5% - 90%RH (non-condensing)
6.4	Cooling:	Free air cooling
6.5	SIZE	90*118*40mm (L*W*H) DIN rail mountable

7..1 RJ45 pin assignment: @5G BASE

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA+	Data Pair A+	-Vdc +BI_DA+	power(-)+Data Pair A+
2	BI_DA-	Data Pair A-	-Vdc +BI_DA-	power(-)+Data Pair A-
3	BI_DB+	Data Pair B+	+Vdc +BI_DB+	power(+)+Data Pair B+
4	BI_DC+	Data Pair C+	+Vdc + BI_DC+	power(+)+Data Pair C+
5	BI_DC-	Data Pair C-	+Vdc + BI_DC-	power(+)+Data Pair C-
6	BI_DB-	Data Pair B-	+Vdc +BI_DB-	power(+)+Data Pair B-
7	BI_DD+	Data Pair D+	-Vdc + BI_DD+	power(-)+Data Pair D+
8	BI_DD-	Data Pair D-	-Vdc + BI_DD-	power(-)+Data Pair D-
S	Shield	Shield	Shield	Shield

7.2 the input and output RJ45s with metal enclosure for shield, connected to FG.

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802.3BT :

Table 1. IEEE-Specified Power Allocations, Single-Signature PD

PD CLASS	PSE OUTPUT POWER	ALLOCATED CABLING LOSS	PD INPUT POWER
1	4W	0.16W	3.84W
2	6.7W	0.21W	6.49W
3	14W	1W	13W
4	30W	4.5W	25.5W
5	45W	5W	40W
6	60W	9W	51W
7	75W	13W	62W
8	90W	18.7W	71.3W

Table 2. PSE Maximum Delivered Power, Per-Port

DEVICE			PSE			
	STANDARD		802.3at		802.3bt	
		TYPE	1	2	3	4
PD	802.3at	1	13W	13W	13W	13W
		2	13W*	25.5W	25.5W	25.5W
	802.3bt	3	13W*	25.5W*	51W	51W
		4	13W*	25.5W*	51W*	71.3W

*Indicates PD allocated less power than requested.

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Table 4. Typical Auto Mode Power On Thresholds, Single-Signature PD

	PER-CHANNEL		PER-PORT
CLASS	I_{CUT-2P}	I_{LIM-2P}	P_{CUT-4P}
1	94mA	425mA	5.43W
2	150mA	425mA	8.69W
3	338mA	425mA	19.5W
4	638mA	850mA	36.4W
5	581mA	850mA	52.7W
6	731mA	850mA	70.0W
7	825mA	1063mA	87.4W
8	975mA	1167mA	96.6W

Table 5. Typical Auto Mode Power On Thresholds, Dual-Signature PD

	PER-CHANNEL		
CLASS	I_{CUT-2P}	I_{LIM-2P}	P_{CUT-2P}*
1	94mA	425mA	5.43W
2	150mA	425mA	8.69W
3	338mA	425mA	19.5W
4	638mA	850mA	36.4W
5	975mA	1167mA	48.3W

*A per-port P_{CUT-4P} threshold holds the sum of P_{CUT-2P} for each powered channel.

Table 9. Type 3 and Type 4 PD Classifications by PD Configuration

SINGLE-SIGNATURE PDs		DUAL-SIGNATURE PDs	
CLASS	PD AVAILABLE POWER	CLASS	CHANNEL AVAILABLE POWER*
Class 1	3.84W	Class 1	3.84W
Class 2	6.49W	Class 2	6.49W
Class 3	13W	Class 3	13W
Class 4	25.5W	Class 4	25.5W
Class 5	40W	Class 5	35.6W
Class 6	51W		
Class 7	62W		
Class 8	71.3W		

*Dual-signature PD total available power is the sum of both channels available power. Class signatures may differ between channels of a port, e.g., Class 3 + Class 4 = 13W + 25.5W = 38.5W.

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