

PRODUCT DESCRIPTION:

Layer 1 L1-OP3910D is a compact 10Gb/s optical transceiver module for serial optical communication applications at 10Gb/s. The L1-OP3910D converts a 10Gb/s serial electrical data stream to 10Gb/s optical output signal and a 10Gb/s optical input signal to 10Gb/s serial electrical data streams. The high speed 10Gb/s electrical interface is fully compliant with SFI specification.



The high performance 1310nm DFB transmitter and high sensitivity PIN receiver provide superior performance for Ethernet applications at up to 10km links.

The SFP+ Module compliant with SFF-8431, SFF-8432 and IEEE 802.3ae 10GBASE-LR. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

The fully SFP compliant form factor provides hot pluggable, easy optical port upgrades and low EMI emission.

KEY FEATURES

- ◆ Supports 8.5 to 11.3Gb/s bit rates
- ◆ Hot-Pluggable
- ◆ Uncooled 1310nm DFB laser
- ◆ Duplex LC connector
- ◆ RoHS compliant and Lead Free
- ◆ Up to 10 km on 9/125µm SMF
- ◆ Single +3.3V Power Supply
- ◆ Built-in digital diagnostic functions
- ◆ Commercial operating temperature range: 0°C to 60°C

APPLICATIONS

- ◆ 10GBASE-LR/LW 10G Ethernet
- ◆ 1200-SM-LL-L 10G Fibre Channel
- ◆ SONET OC-192 / SDH STM-64

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Supply Voltage	V _{CC}	0		3.6	V
Storage Temperature	T _S	-40		85	°C
Operating Case Temperature	T _A	0		70	°C
Relative Humidity	RH	0		85	%
RX Input Average Power	P _{max}	-		0	dBm

RECOMMENDED OPERATING ENVIRONMENT

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Supply Voltage	V _{CC}	3.135	3.3	3.465	V
Power Supply Current	I _{CC}			300	mA
Operating Case Temperature	T _C	0	25	60	°C

ELECTRICAL CHARACTERISTICS (TOP = 0 TO 70 °C, VCC = 3.0 TO 3.60 VOLTS)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	Vcc	3.135		3.465	V	
Supply Current	Icc			300	mA	
Transmitter Section:						
Input differential impedance	R_{in}		100		Ω	1
Single ended data input swing	$V_{in PP}$	180		700	mVp-p	2
Transmit Disable Voltage	V_D	$V_{cc} - 1.3$		Vcc	V	3
Transmit Enable Voltage	V_{EN}	Vee		Vee+ 0.8	V	
Receiver Section:						
Single ended data output swing	$V_{out,pp}$	300		850	mv	
Data output rise time and fall time	t_r / t_f	30			ps	4
LOS Fault	$V_{losfault}$	2		V_{CC_host}	V	5
LOS Normal	$V_{los norm}$	V_{ee}		$V_{ee}+0.8$	V	5

Note:

1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
2. Per SFF-8431 Rev 3.0
3. Into 100 kohms differential termination.
4. 20 – 80%. Measured with Module Compliance Test Board and OMA test pattern. Use of four 1's and four 0's sequence in the PRBS 9 is an acceptable alternative. SFF-8431 Rev 3.0.
5. LOS is an open collector output. Should be pulled up with 4.7k Ω – 10k Ω on the host board. Normal operation is logic 0; loss of signal is logic 1.

OPTICAL PARAMETERS (TOP = 0 TO 70 °C, VCC = 3.00 TO 3.60 VOLTS)

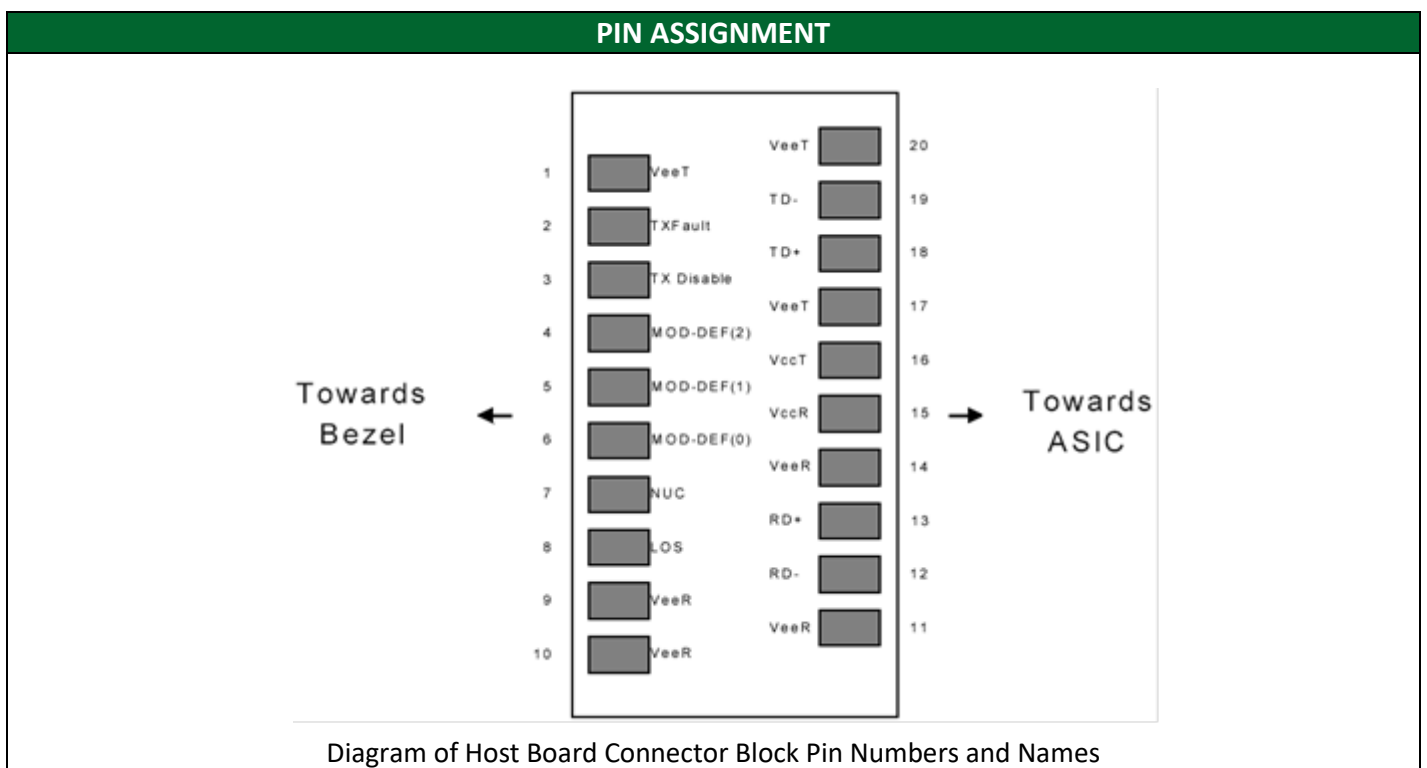
Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter Section:						
Centre Wavelength	λ_t	1260	1310	1355	nm	
spectral width	$\Delta\lambda$			1	nm	
Average Optical Power	P_{avg}	-6		-1	dBm	1
Optical Power OMA	P_{oma}	-5.2			dBm	
Laser Off Power	P_{off}			-30	dBm	
Extinction Ratio	ER	3.5			dB	
Transmitter Dispersion Penalty	TDP			3.2	dB	2
Relative Intensity Noise	R_{in}			-128	dB/Hz	3
Optical Return Loss Tolerance		20			dB	
Receiver Section:						
Centre Wavelength	λ_r	1260		1355	nm	
Receiver Sensitivity	S_{en}			-12.6	dBm	4
Stressed Sensitivity (OMA)	S_{en2}			-10.3	dBm	4
Los Assert	LOS_A	-30		-	dBm	
Los Dessert	LOS_D			-13.5	dBm	
Los Hysteresis	LOS_H	0.5			dB	
Overload	Sat	0			dBm	5
Receiver Reflectance	R_{rx}			-12	dB	

Note:

1. Average power figures are informative only, per IEEE802.3ae.

2. TWDP figure requires the host board to be SFF-8431 compliant. TWDP is calculated using the Matlab code provided in clause 68.6.6.2 of IEEE802.3ae.
3. 12dB reflection.
4. Conditions of stressed receiver tests per IEEE802.3ae. CSRS testing requires the host board to be SFF-8431 compliant.
5. Receiver overload specified in OMA and under the worst comprehensive stressed condition.

TIMING CHARACTERISTICS						
Parameter	Symbol	Min.	Typical	Max.	Unit	
TX_Disable Assert Time	t_off			10	us	
TX_Disable Negate Time	t_on			1	ms	
Time to Initialize Include Reset of TX_FAULT	t_int			300	ms	
TX_FAULT from Fault to Assertion	t_fault			100	us	
TX_Disable Time to Start Reset	t_reset	10			us	
Receiver Loss of Signal Assert Time	T _{A,RX_LOS}			100	us	
Receiver Loss of Signal Deassert Time	T _{d,RX_LOS}			100	us	
Rate-Select Chage Time	t_ratesel			10	us	
Serial ID Clock Time	t_serial-clock			100	kHz	



PIN FUNCTION DEFINITIONS			
PIN #	Name	Function	Notes
1	VeeT	Module transmitter ground	1
2	Tx Fault	Module transmitter fault	2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	2
7	RS0	Rate select0, optionally control SFP+ receiver. When high, input data rate >4.5Gb/ s; when low, input data rate <=4.5Gb/s	
8	LOS	Receiver Loss of Signal Indication	4
9	RS1	Rate select0, optionally control SFP+ transmitter. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	

10	VeeR	Module receiver ground	1
11	VeeR	Module receiver ground	1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	
20	VeeT	Module transmitter ground	1

Notes:

1. The module ground pins shall be isolated from the module case.
2. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.
3. This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.
4. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

SFP Module EEPROM Information and Management

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP -8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h.

The memory is mapped in Table 1.

Detailed ID information (A0h) is listed in Table 2 and the DDM specification at address A2h.

For more details of the memory map and byte definitions, please refer to the SFF-8472, “Digital Diagnostic Monitoring Interface for Optical Transceivers”. The DDM parameters have been internally calibrated.

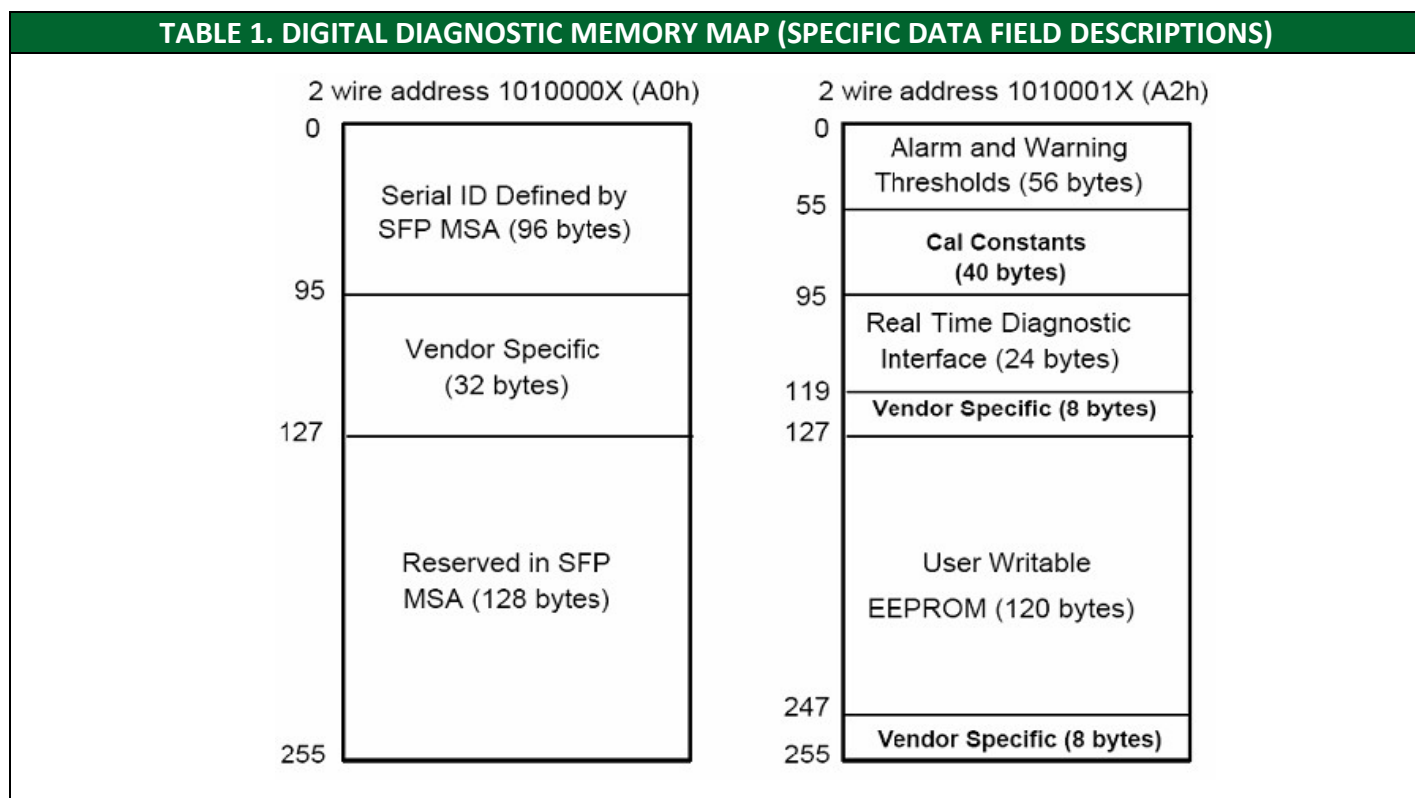


TABLE 2 - EEPROM SERIAL ID MEMORY CONTENTS (A0H)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	10G Base-LR
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fibre, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fibre, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fibre, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: LAYER 1
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "L1-OP3910D" (ASCII)
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	LAYER 1's Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	LAYER 1 specific date, read only
128-255	128	Reserved	Reserved for SFF-8079

DIGITAL DIAGNOSTIC MONITOR CHARACTERISTICS

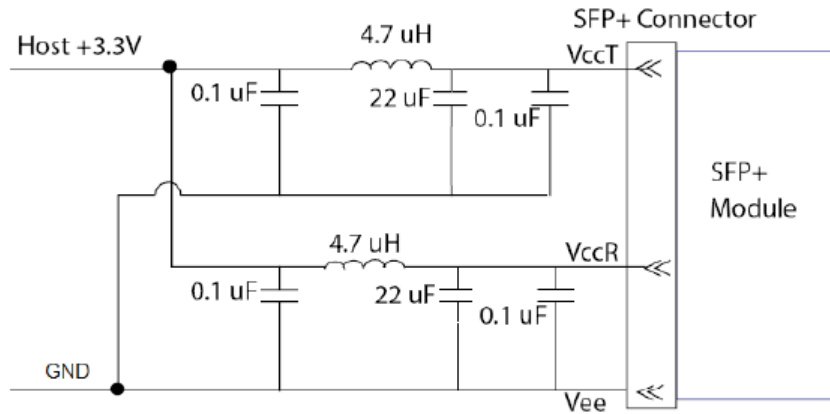
Data Address	Parameter	Accuracy	Unit
96-97	Transceiver Internal Temperature	±3.0	°C
98-99	VCC3 Internal Supply Voltage	±5.0	%
100-101	Laser Bias Current	±10	%
102-103	Tx Output Power	±3.0	dBm
104-105	Rx Input Power	±3.0	dBm

REGULATORY COMPLIANCE

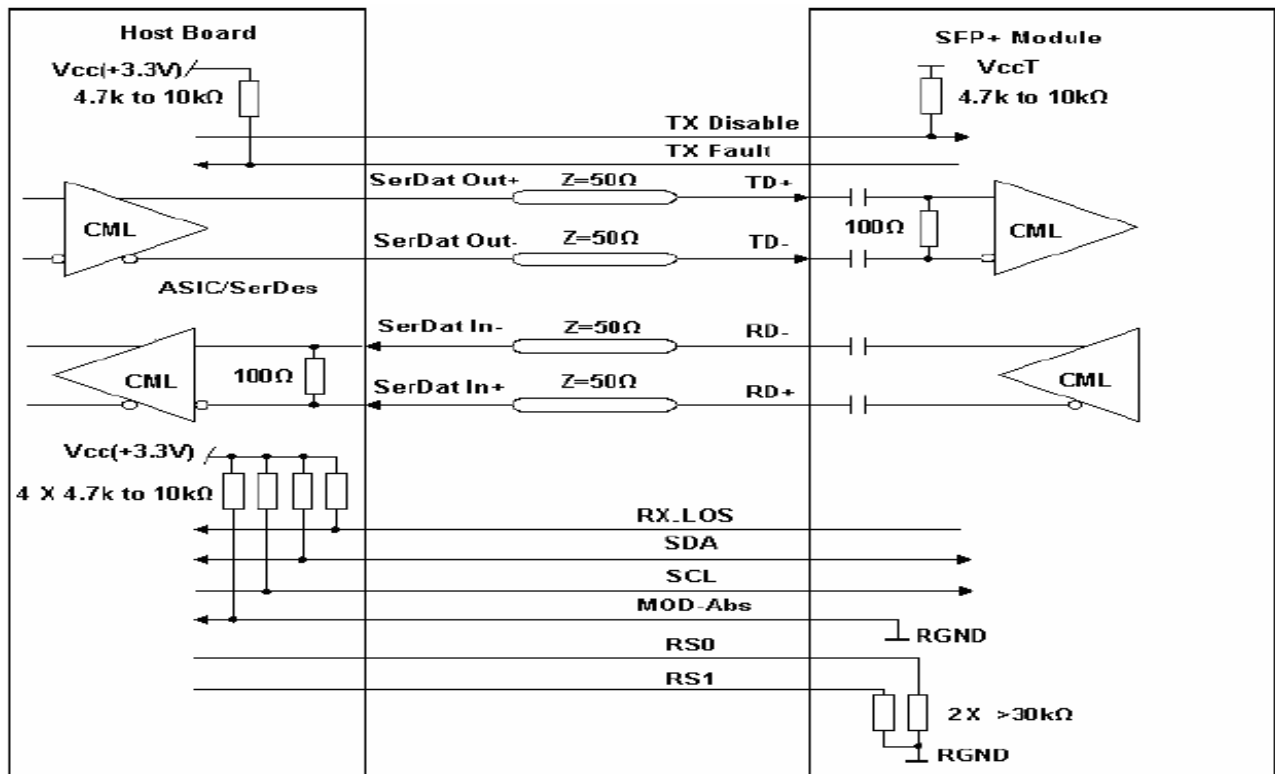
The L1-OP3910D complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000 V)
Electrostatic Discharge (ESD) to the Duplex LC Receptacle	IEC 61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class 1 laser product.

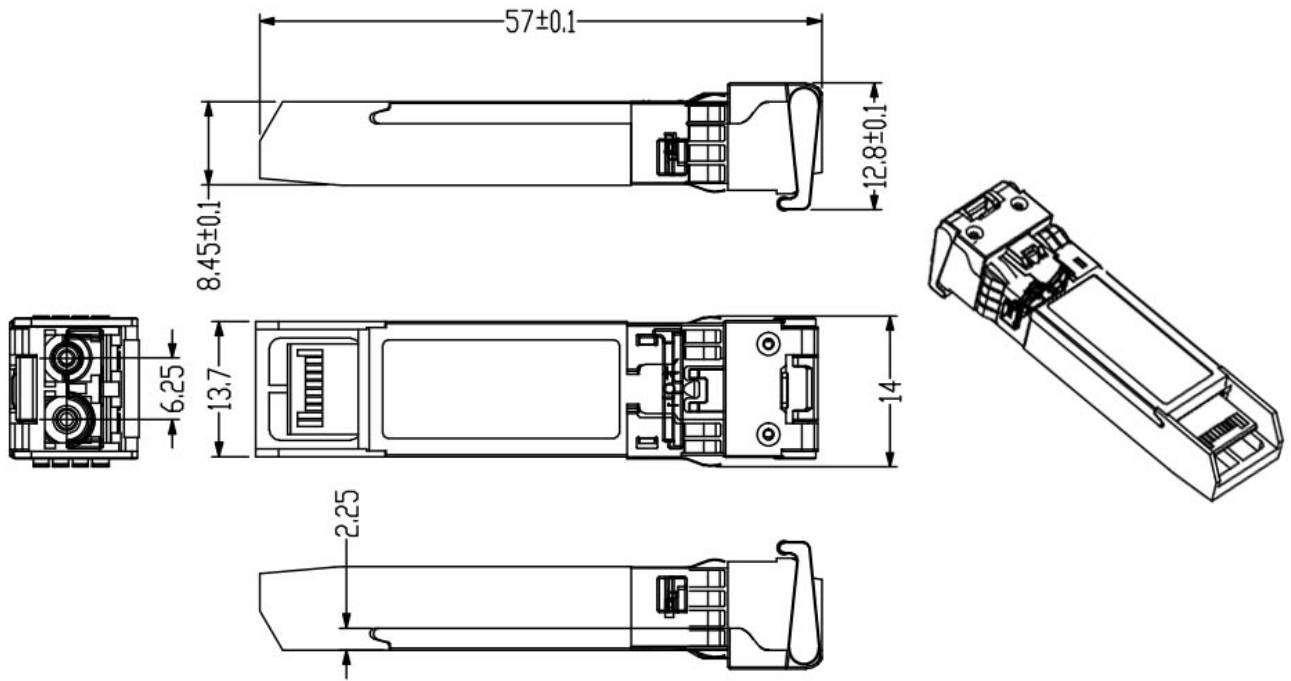
Recommended Circuit



Recommended Host Board Power Supply Circuit



Mechanical Dimensions:



Mechanical Drawing